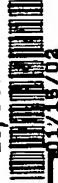


36879 U.S. PTO  
10/046792



# BEST AVAILABLE COPY

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10046792	FILING DATE 01/15/2002	CLASS 257	SUBCLASS 508	GAU 2841	EXAMINER <i>2015</i> <i>Edie Lee</i>
<b>**APPLICANTS:</b> Natsume Hidetaka; <i>Nguyen</i>					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-013330 01/22/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO NECW 19,349	
<b>TITLE :</b> Semiconductor memory device and manufacturing method thereof <small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-436L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>JOSEPH NGUYEN</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner		Total Claims 13	Print Claim for O.G. 1
<b>ISSUE FEE</b>				<b>DRAWING</b>	
Amount Due	Date Paid			Sheets Drwg. 38	Figs. Drwg. 65
		Primary Examiner		Print Fig. 1	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>		<b>Application Examiner</b>	
<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.					

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)